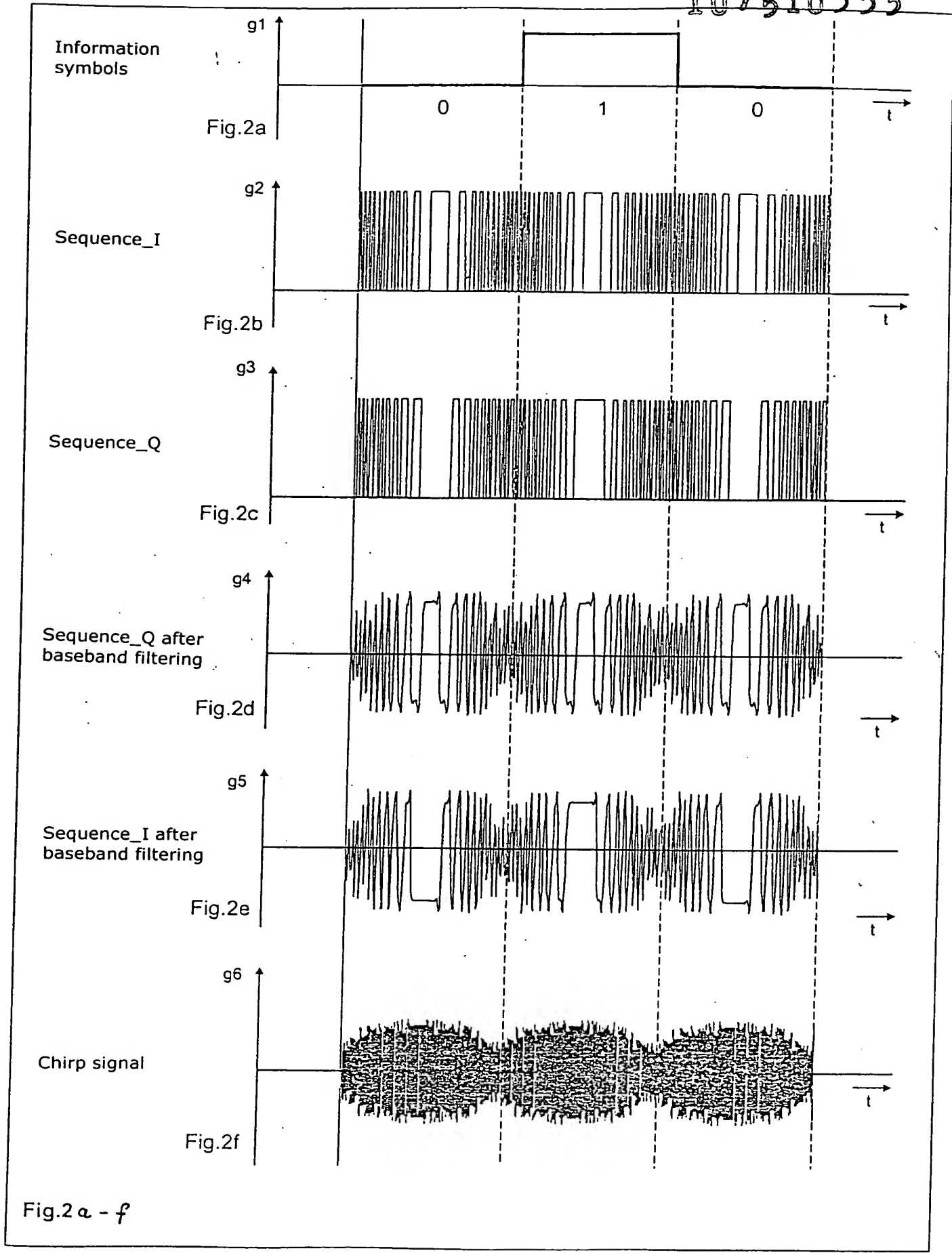
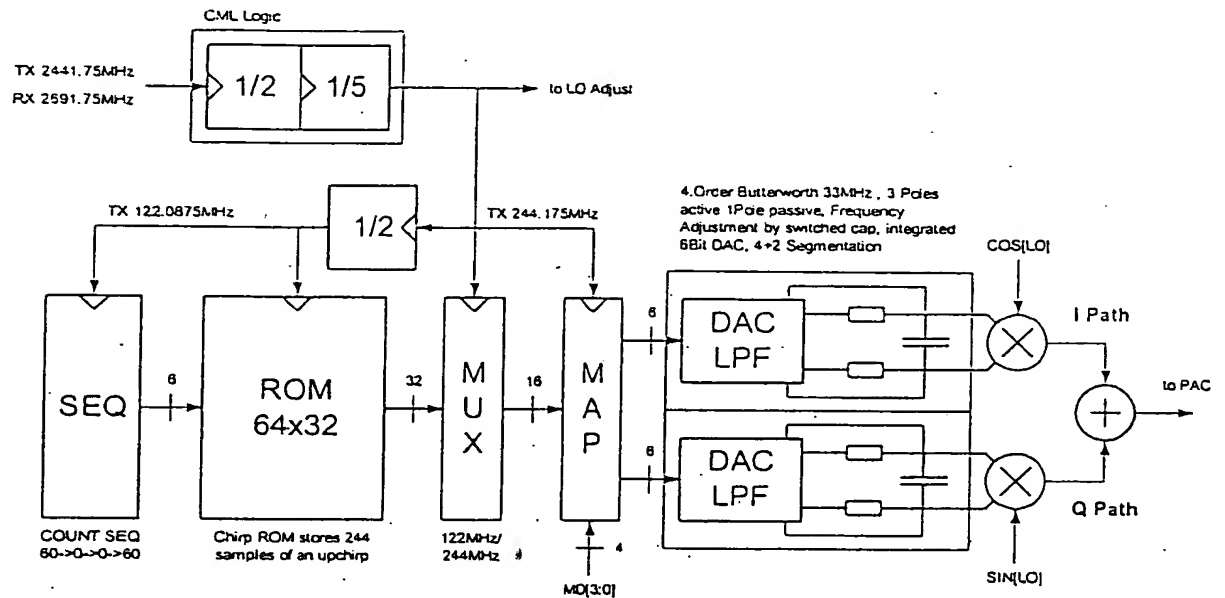


Fig. 1



Chirp Generation



IQ ROM to IQ DAC Mapping Definition

MD[3]	MD[2]	MD[1]	MD[0]	IDAC	QDAC	Description
Basic Modes						
0	0	0	0	IROM	QROM	Upchirp
0	1	0	0	IROM	-QROM	Downchirp
1	0	x	x	IROM	0	Upchirp+Downchirp
1	1	x	x	0	QROM	Upchirp-Downchirp
QPSK Up Modes						
0	0	0	0	IROM	QROM	0
0	0	0	1	QROM	-IROM	Pi/2
0	0	1	0	-IROM	-QROM	Pi
0	0	1	1	-QROM	IROM	3Pi/2
QPSK Down Modes						
0	1	0	0	IROM	-QROM	0
0	1	0	1	QROM	IROM	Pi/2
0	1	1	0	-IROM	QROM	Pi
0	1	1	1	-QROM	-IROM	3Pi/2

Fig. 2g

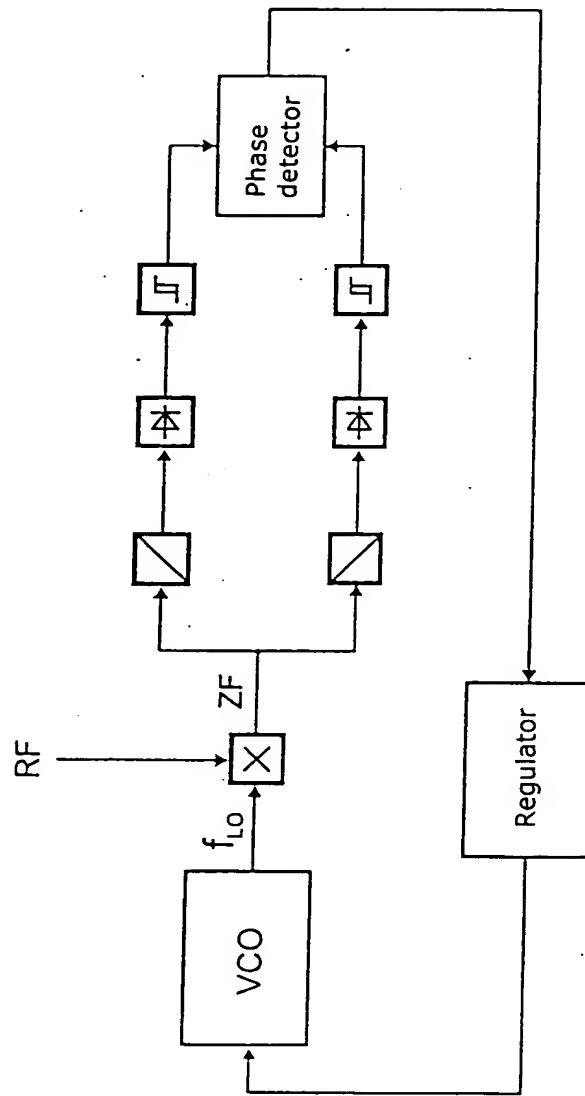


Fig. 3

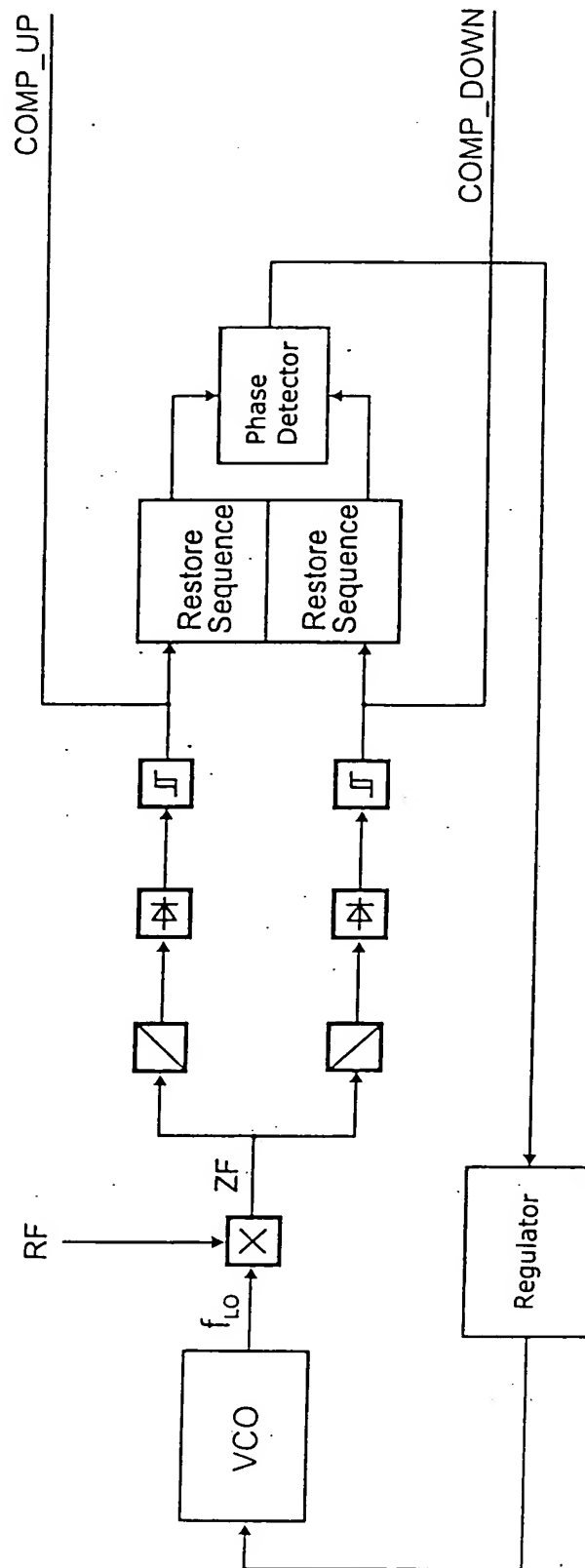


Fig. 4

10/510553

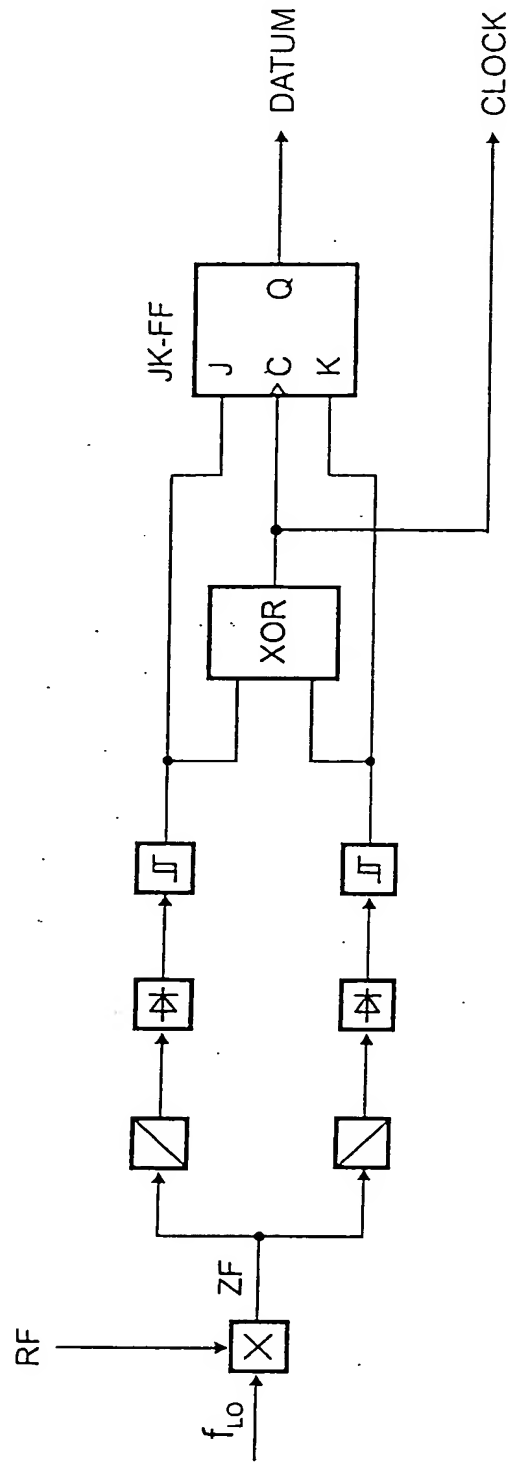


Fig. 5

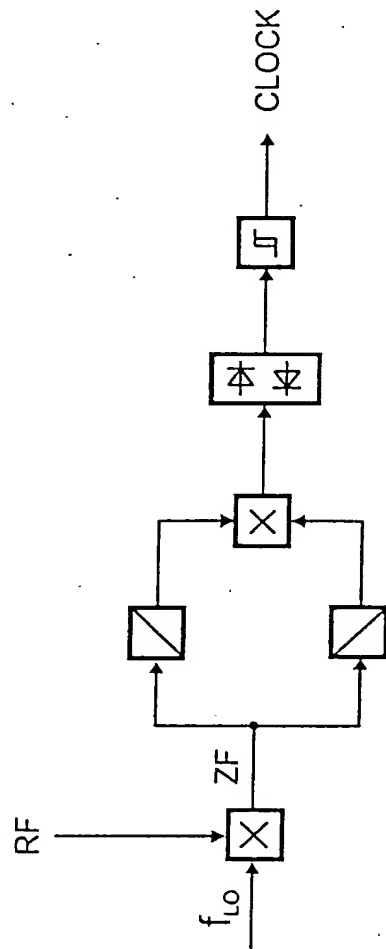


Fig. 6

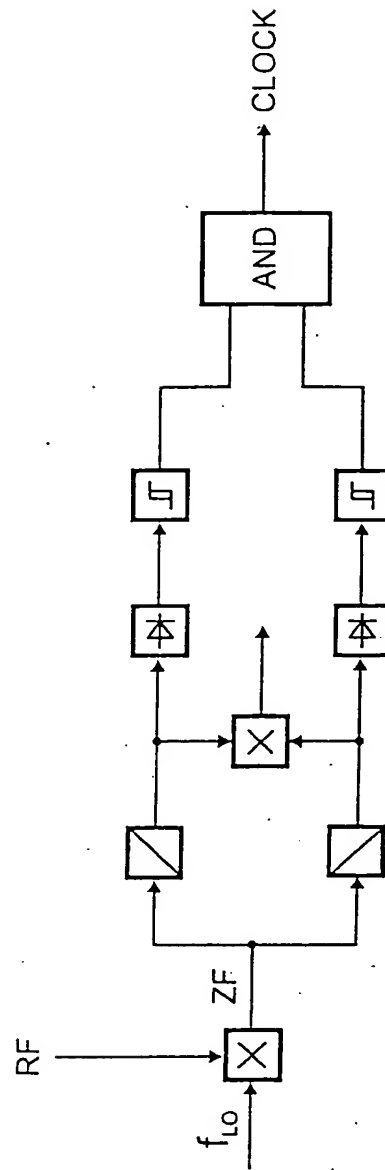


Fig. 7

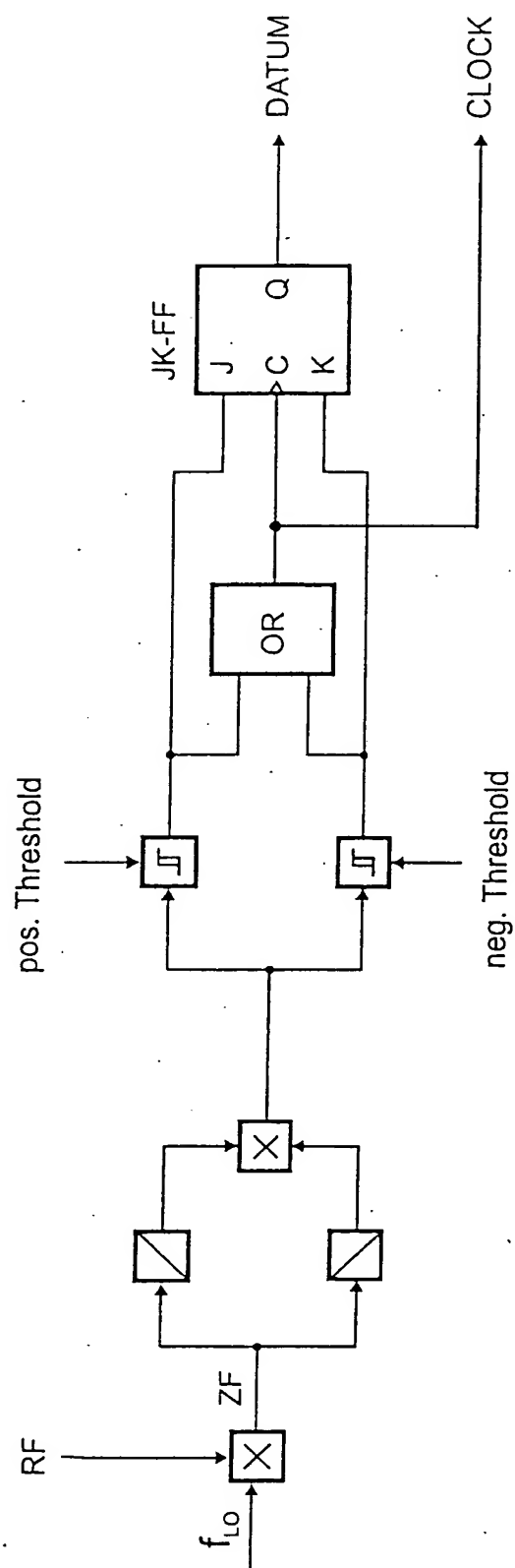


Fig. 8

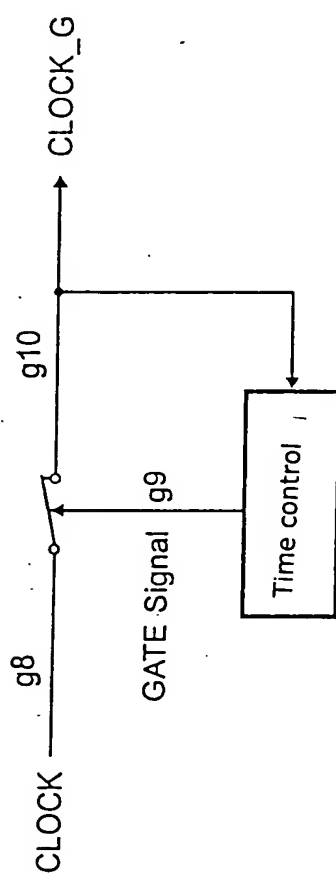


Fig. 9

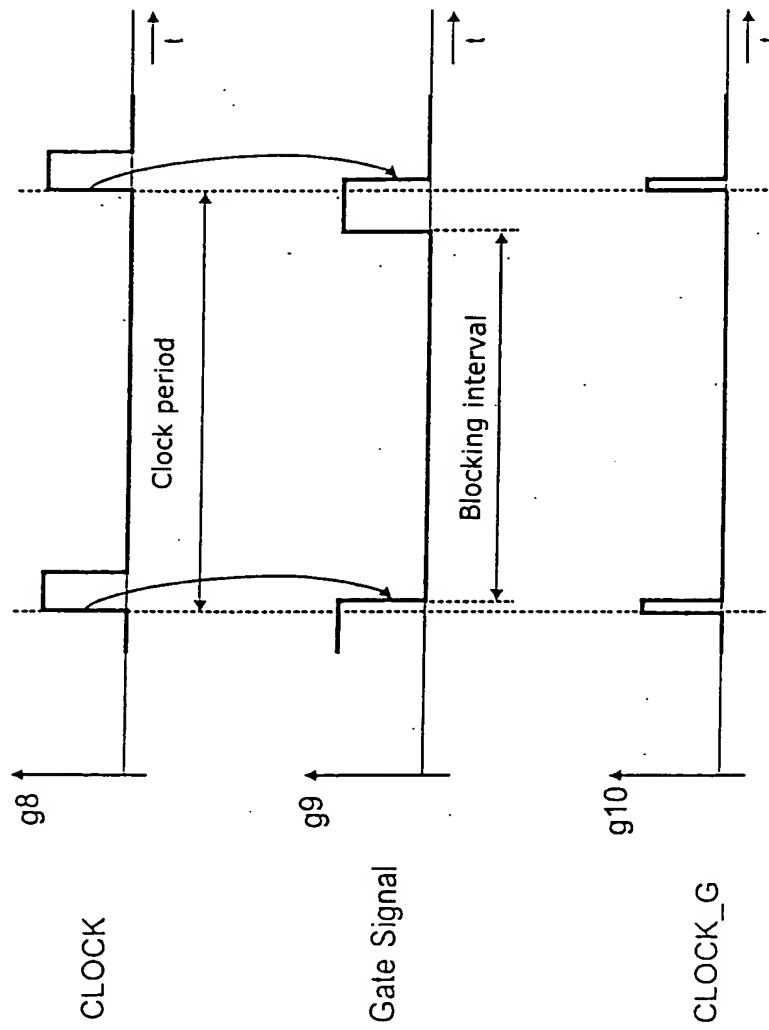


Fig. 10

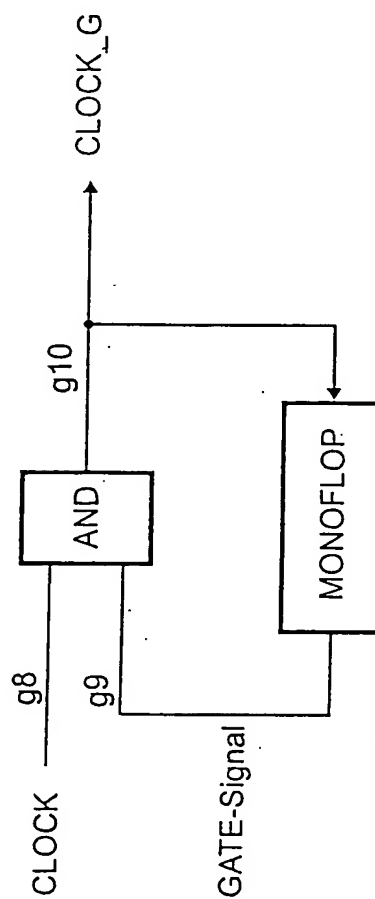


Fig. 11

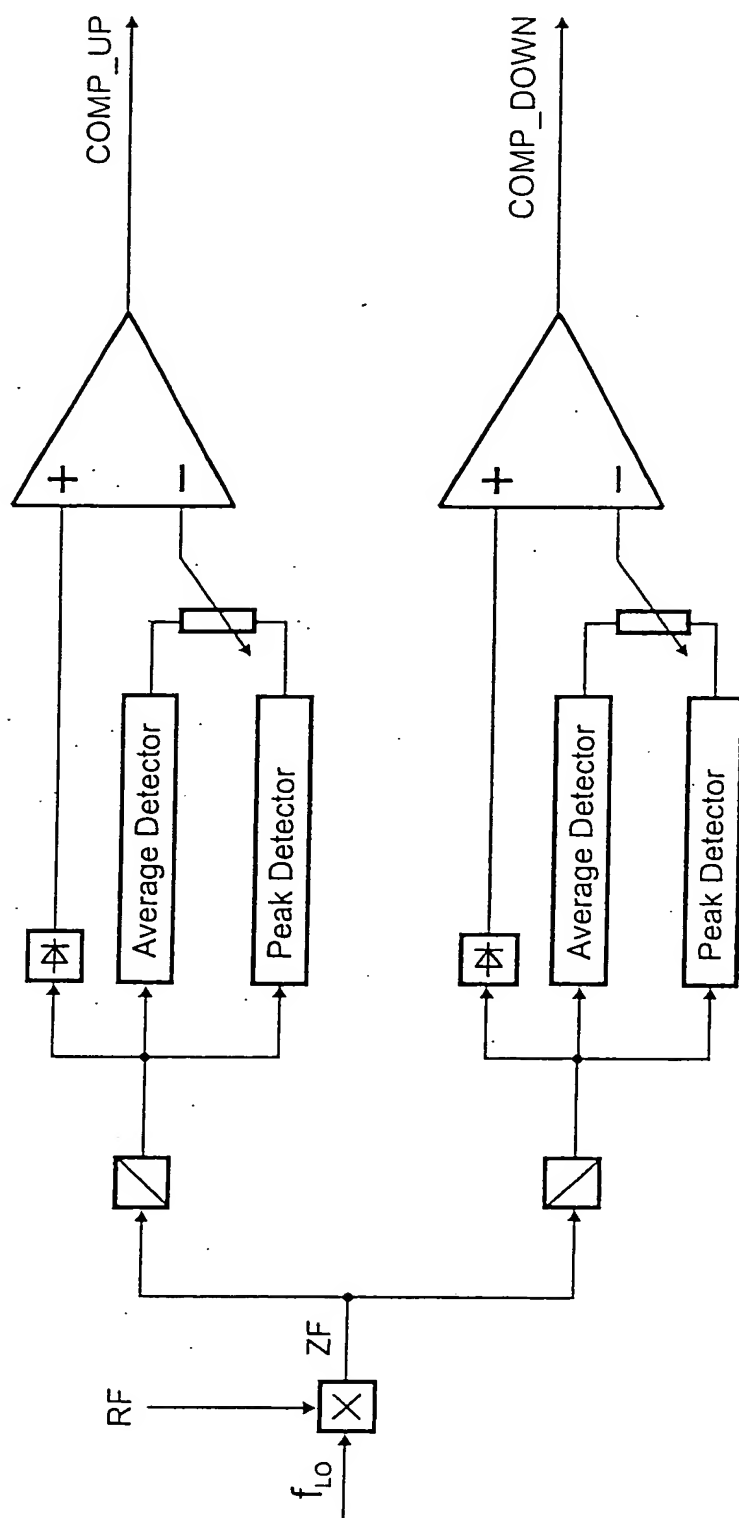


Fig. 12

10/510553

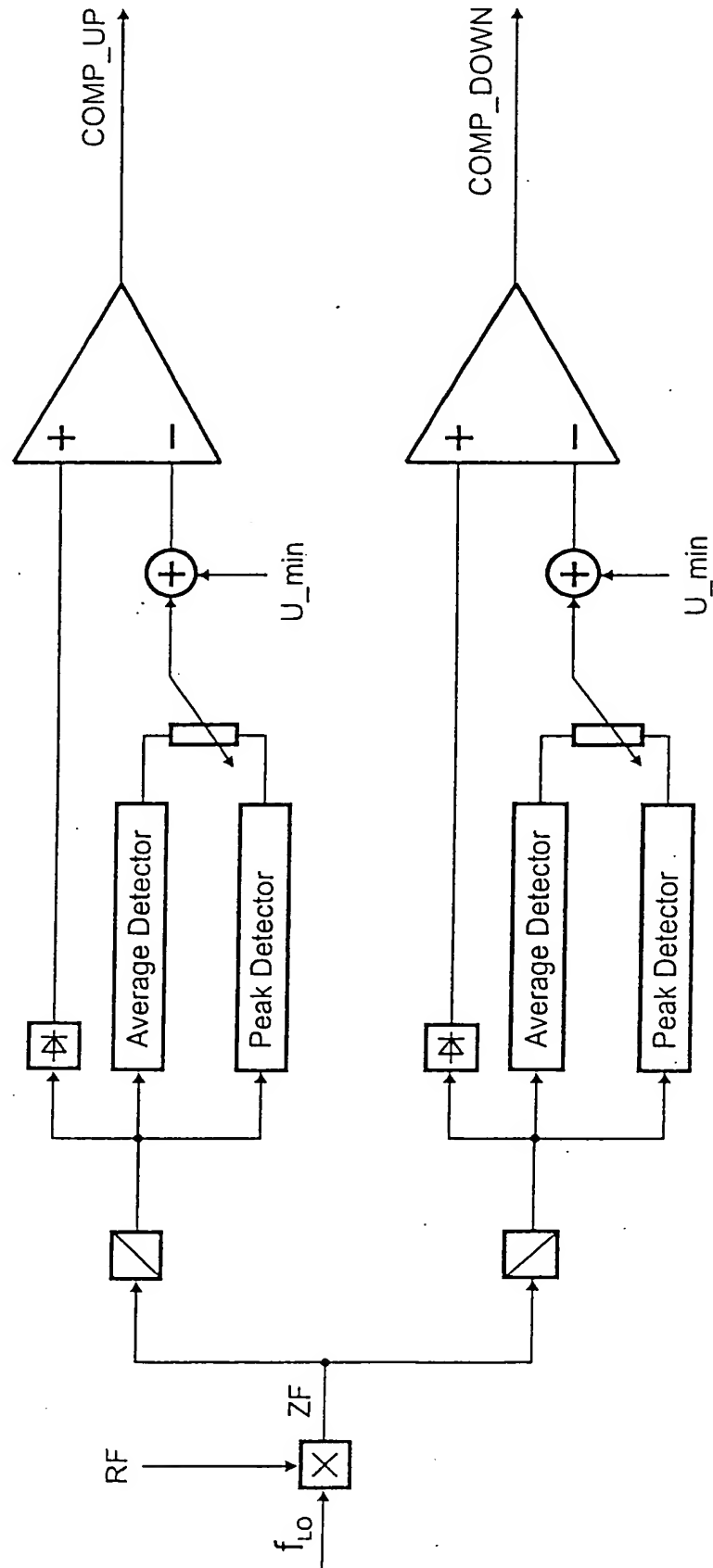


Fig. 13

10/510553

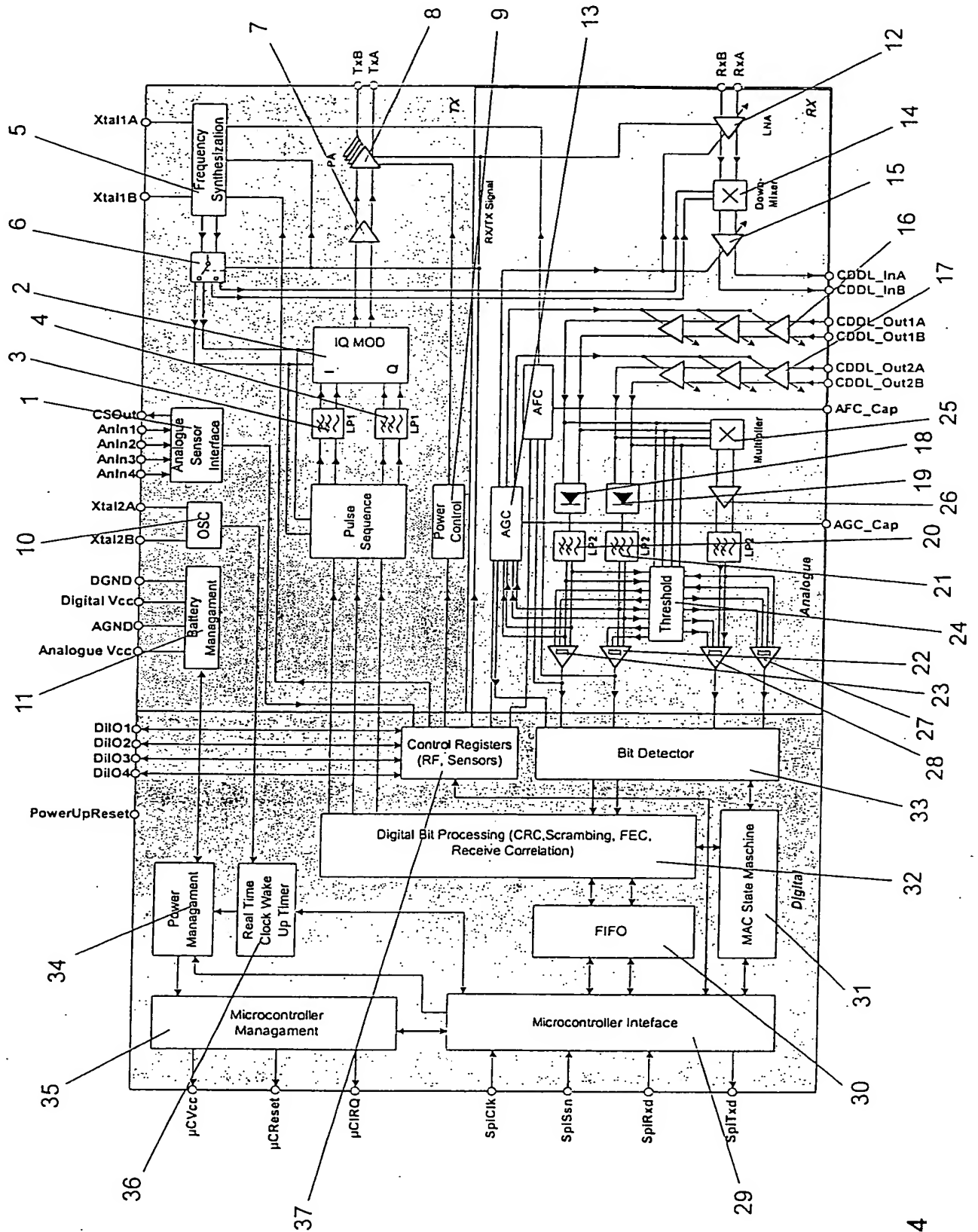


Fig. 14

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Differential
Comparator

A > B then Out = 1
Otherwise Out = 0

DDDL

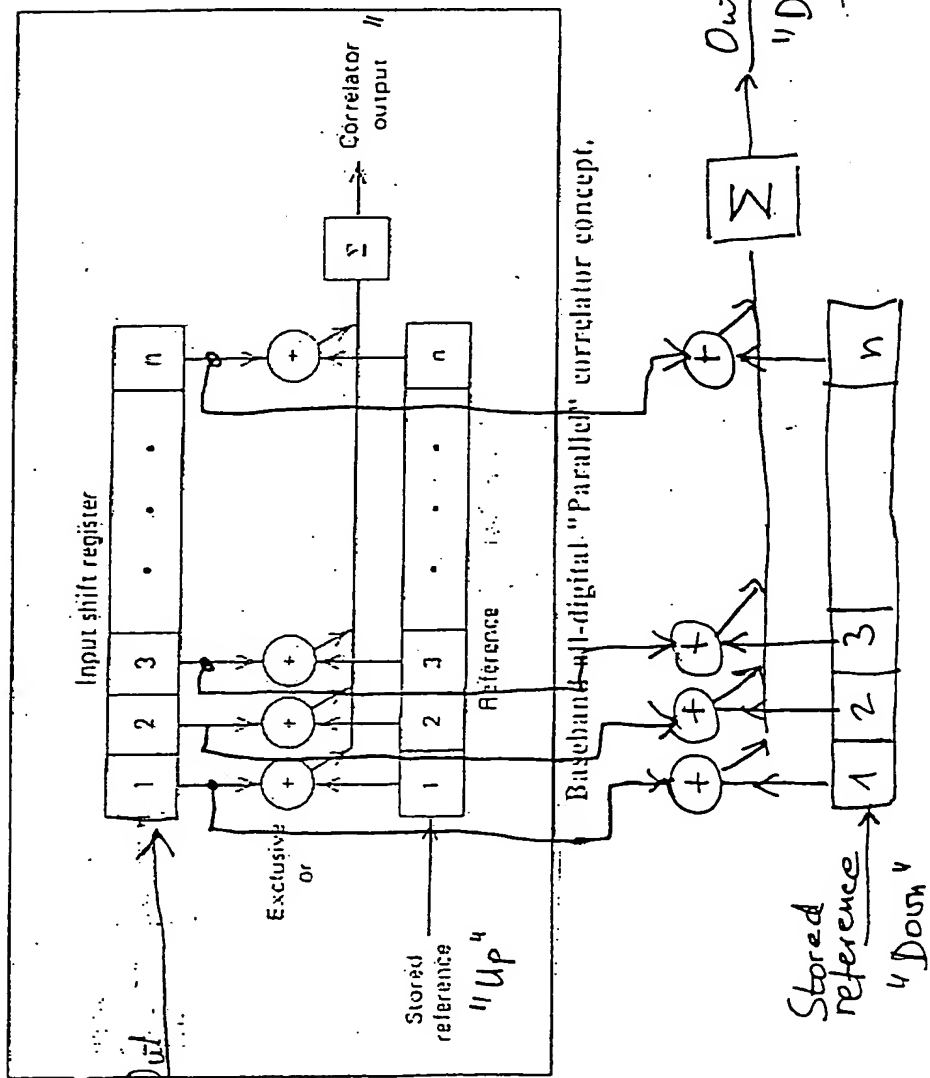


Fig. 15

Chirp detected

Comparator
"Up"

"Up"

Threshold

10/510555

Comparator
"Down"

Output
"Down"

Chirp detected

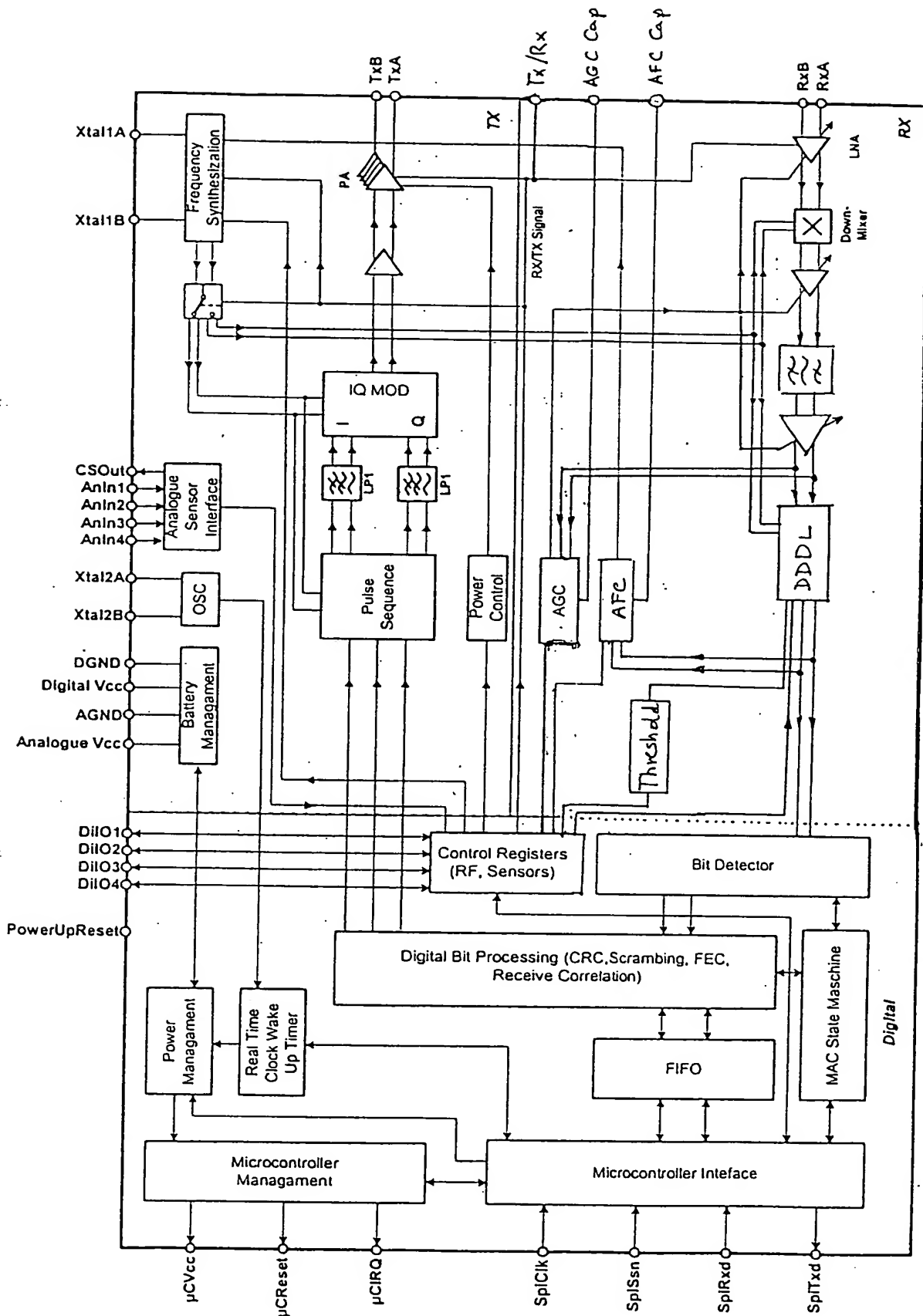


Fig. 16